

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
6	BRS	L6	12	((EDWARD) near2 (AUNG)).INV.	US- PGPUB ; USPAT ; USOCR	2006/09/0 1 22:07	
7	BRS	L7	20	((HENRY) near2 (LUI)).INV.	US- PGPUB ; USPAT ; USOCR	2006/09/0 1 22:07	
8	BRS	L8	44	((PAUL) near2 (BUTLER)).INV.	US- PGPUB ; USPAT ; USOCR	2006/09/0 1 22:07	
9	BRS	L10	112	((RAKESH) near2 (PATEL)).INV.	US- PGPUB ; USPAT ; USOCR	2006/09/0 1 22:08	
10	BRS	L9	734	((JOHN) near2 (TURNER)).INV.	US- PGPUB ; USPAT ; USOCR	2006/09/0 1 22:08	
11	BRS	L11	206	((CHONG) near2 (LEE)).INV.	US- PGPUB ; USPAT ; USOCR	2006/09/0 1 22:08	

	<b>Error Definition</b>	<b>Err ors</b>
<b>6</b>		
<b>7</b>		
<b>8</b>		
<b>9</b>		
<b>10</b>		
<b>11</b>		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
12	BRS	L12	869	6 or 7 or 8 or 9 or 10 or 11	US- PGPUB ; USPAT ; EPO; JPO	2006/09/0 1 22:10	
13	BRS	L13	16	12 and (PLD with CDR)	US- PGPUB ; USPAT ; EPO; JPO	2006/09/0 1 22:12	
14	BRS	L15	6	(CDR or clock adj2 data adj2 recover\$3) same (PLD\$1 or programmable adj2 logic\$6 adj2 device\$1 or programmable adj2 logic\$6 adj2 circuit\$3) same (serial adj2 data) same (parallel adj2 data or "P/S" or "S/P")	US- PGPUB ; USPAT ; EPO; JPO	2006/09/0 1 22:18	
15	BRS	L14	47	(CDR or clock adj2 data adj2 recover\$3) same (PLD\$1 or programmable adj2 logic\$6 adj2 device\$1 or programmable adj2 logic\$6 adj2 circuit\$3) same (external near3 clock\$1 or reference near2 clock or local adj2 lock or master clock\$1 or outside near3 clock\$3)	US- PGPUB ; USPAT ; EPO; JPO	2006/09/0 1 22:16	

	Error Definition	Err ors
12		
13		
14		
15		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
16	BRS	L16	29	(CDR or clock adj2 data adj2 recover\$3) same (PLD\$1 or programmable adj2 logic\$6 adj2 device\$1 or programmable adj2 logic\$6 adj2 circuit\$3) same (serial adj2 data or serial\$6 or deserial\$6 or "P/S" or "S/P")	US- PGPUB ; USPAT ; EPO; JPO	2006/09/0 1 22:19	
17	BRS	L17	29	15 or 16	US- PGPUB ; USPAT ; EPO; JPO	2006/09/0 1 22:19	

	Error Definition	Err ors
16		
17		